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PTO/SB/81 (02-01)

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Application Number	10/628,036
Filing Date	July 24, 2003
First Named Inventor	Amit Ramchandran
Group Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	021202-003740US

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Certificate under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name Laura J. Durr, Director of Finance and Administration

Signature

Date

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Amit Ramchandran
Serial No. : 10/628,036
Filed : July 24, 2003
Title : CACHE FOR INSTRUCTION SET ARCHITECTURE

Art Unit : Unassigned
Examiner : Unassigned

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Under 37 CFR §3.73(b) QuickSilver Technology, a corporation, certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of:

☒ An assignment from the inventors of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel 014956, Frame 0797 on 02/09/2004.

The undersigned, whose title is supplied below, is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Respectfully submitted,

Date: 1-06-2004

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